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TC 1700

Re: Applicant(s): Institute of Microelectronics
 Inventor: Ranganathan Nagarajan
 Serial No. 09/900,293
 Filing Date: July 6, 2001
 For: Sloped Trench Etching Process
 Docket No.: 33726-16USPX

Dear Sir:

Transmitted for filing with the Patent and Trademark Office are the following documents for the above-referenced patent application:

1. Claim of Priority Under 35 U.S.C. § 119
2. Certified copy of Singapore Patent No. 200102727-5
3. Acknowledgment Postcard

Please address all communications related to this to:

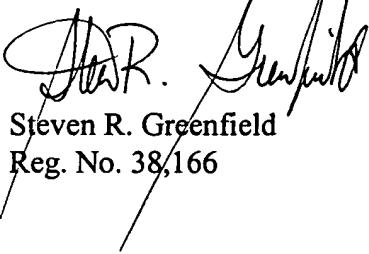
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Respectfully submitted,

JENKENS & GILCHRIST,
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Dated: Nov 16, 2001

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